

UPWARDS Summer Intensive Program 2026 – Information Sheet

Host University	Institute of Science Tokyo (Science Tokyo)
Program Title	Science Tokyo UPWARDS Summer Program 2026
Program Period (Dates)	From August 21 to August 28, 2026
Location & Venue	Institute of Science Tokyo (Ookayama Campus) Tokyo, Japan
Maximum Enrollment from Partner Universities	12 students in total (2 students per U.S. partner university)
Program Fee	1,500 USD per student (Accommodation fees, meal expenses, and public transportation costs are not included.)
Requirements	<p>Students currently enrolled in undergraduate or graduate programs at an UPWARDS member university</p> <p>Students with experience in programming, logic design, computer systems, AI, etc., and a strong interest in semiconductor technology are encouraged to apply.</p>
Application Method & Deadline	<p>Application Procedure</p> <ul style="list-style-type: none"> • Applications must be submitted through the applicant’s home university. Participants should first inform their university’s UPWARDS Program coordinator of their intention to apply for this program. • UPWARDS coordinators should notify the Science Tokyo UPWARDS Summer Program office and submit the designated nomination sheet by the deadline via the URL below: https://science-tokyo.app.box.com/f/387a6ae6f98844c7b68be0d7ab5ce526 • The Science Tokyo UPWARDS Summer Program office will then directly request application documents from participants, who must submit the required documents by the deadline below. Nomination Sheet Deadline: March 16, 2026 (Mon), 17:00 JST Application Documents Deadline: March 27, 2026 (Fri), 17:00 JST • Applications received after the deadline will not be accepted. Submitted documents will not be returned for any reason. • Notification of acceptance for the Science Tokyo UPWARDS Summer Program will be sent by April 15, 2026. <p>Application Documents (Those marked * are mandatory for all applicants)</p> <ul style="list-style-type: none"> • Response to questionnaire via Forms (Application Form)* • Copy of passport page showing photo and passport number* • Certificate of enrollment* • Academic transcript* • JPEG photo file* • Letter of recommendation (prepared by a faculty member at the home university)* • Proof of English proficiency (TOEIC, TOEFL, or certificate issued by the home

	<p>university). Not required for applicants from countries where English is an official language.</p> <ul style="list-style-type: none"> • Visa application documents (COE application) • Additional documents may be required depending on research content (e.g., vaccination certificate for medical/dental fields, portfolio for architecture).
Curriculum Overview	<p>Microprocessor Experiential Learning Program (tentative)</p> <p>August 21 (Fri) Orientation, Campus Tour, Lecture, Welcome Party</p> <p>August 22 (Sat) Free</p> <p>August 23 (Sun) Free</p> <p>August 24 (Mon) Microprocessor Lab</p> <p>August 25 (Tue) Microprocessor Lab</p> <p>August 26 (Wed) Microprocessor Lab</p> <p>August 27 (Thu) Microprocessor Lab</p> <p>August 28 (Fri) Student Presentations, Closing Ceremony, Farewell Party</p> <ul style="list-style-type: none"> • AI application development using RISC-V microprocessors. • Learn to use our Institute's C2RTL hardware-software co-design environment and experience cutting-edge AI chip development methodologies. • During the above period, participants will also experience Japanese culture.
Competencies to be Acquired (Multiple selections allowed)	<p><input checked="" type="checkbox"/> Competency 1 Foundational Knowledge in Semiconductor Engineering</p> <p><input type="checkbox"/> Competency 2 Foundational Knowledge in Semiconductor Industry and Business</p> <p><input type="checkbox"/> Competency 3 Ability to Manage/Understand Diversity, Equity, and Inclusion</p> <p><input checked="" type="checkbox"/> Competency 4 Gain Global/Inter-Cultural Competencies including International Mindset, Inter-Cultural Competence, Collaboration and Teamwork, Communication Skills, and Leadership</p> <p><input type="checkbox"/> Competency 5 Hands-on Experiences of Semiconductor Industry Operations</p> <p><input checked="" type="checkbox"/> Competency 6 Research Application Skills</p>
Study Hours	40 Hours
Information about Accommodation	Information on accommodation near the Institute will be provided. Participating students should arrange their own lodgings in accordance with the instructions of their home university regarding fees.
Program Coordinator	Takahiro Sasaki
Contact Information	upwards.summer@adm.isct.ac.jp Global Education Division
Program Website & Other Information	