



HIROSHIMA UNIVERSITY

**UPWARDS**  
**Summer Intensive Program 2026**

# PEACE AND SEMICONDUCTOR INNOVATION

A 12-day program at Hiroshima University combining semiconductor learning and peace education. Visit cleanrooms, factories and network with experts. Experience Japanese culture and cross-cultural learning.



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## MAIN SCHEDULE

**Week 1:** Orientation, Campus Tour, Networking with TEL, Micron Factory Tour

**Week 2:** CMOS training, Peace Learning, Saijo City (Ehime) Visit, Shimanami Kaido Island-Hopping Tour

**Final Day:** Student Presentation & Farewell Ceremony

## PROGRAM INFORMATION

**Dates:** July 22 - August 2, 2026

**Location:** Hiroshima University, Japan

**Tuition Fee:** 2,500 USD

**Target:** 1- 2 Students from UPWARDS partner universities

**Website:** [upwards.hiroshima-u.ac.jp](https://upwards.hiroshima-u.ac.jp)

## PROGRAM COMPETENCIES

- ☑ Competency 2 - Foundational Knowledge in Semiconductor Industry and Business
- ☑ Competency 4 - Gain Global/Inter-Cultural Competencies including International Mindset, Inter- Cultural Competence, Collaboration and Teamwork, Communication Skills, and Leadership
- ☑ Competency 5 - Hands-on Experiences of Semiconductor Industry Operations

## CONTACT

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# UPWARDS Summer Intensive Program 2026

Date	Location	AM	PM	Evening
<b>Day 1</b> Jul 22 (Wed)	Hotel	Arrival		
<b>Day 2</b> Jul 23 (Thu)	Hiroshima University	Welcome, Orientation Campus Tour	Ice-breaking & Networking with TEL	Buffer Time
<b>Day 3</b> Jul 24 (Fri)	Micron + Okosta (Hiroshima station)	Micron Visit	Buffer Time	Let's try Okonomiyaki
<b>Day 4</b> Jul 25 (Sat)	Holiday	Free		
<b>Day 5</b> Jul 26 (Sun)	Holiday	Free		
<b>Day 6</b> Jul 27 (Mon)	Hiroshima University	Lab Internship (Joint Program: Semiconductor CMOS Practical Training - Transistor Layout Design)		
<b>Day 7</b> Jul 28 (Tue)	Hiroshima Peace Memorial Park + Museum	Peace Study (Hiroshima Peace Memorial Park)	Move to Ehime	
<b>Day 8</b> Jul 29 (Wed)	Saijo City, Ehime Prefecture + Shimanami	Saijo City, Ehime Prefecture Visit	Shimanami Kaido Tour	Return to Higashi - Hiroshima
<b>Day 9</b> Jul 30 (Thu)	Hiroshima University	Process: Lecture & Cleanroom Tour		Networking with CMOS participants
<b>Day 10</b> Jul 31 (Fri)	Hiroshima University	Lab Internship (Joint Program: Semiconductor CMOS Practical Training - Transistor Measurement)		
<b>Day 11</b> Aug 1 (Sat)	Hiroshima University	Final Presentations	Closing Ceremony & Farewell Party	
<b>Day 12</b> Aug 2 (Sun)	Hotel	Departure		